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Title: POWER UP INITIALIZATION FOR MEMORY

IN THE SPECIFICATION

Please replace Paragraph [0037] of Page 9 of the Present Application with the following amended Paragraph:

[0037] However, a Flash memory device of the present invention does require a software "STOP" signal to stop the initialization cycle. The software signal must be issued to a Flash memory of the present invention by the host controller device. This requires in some cases the appropriate software routines to be loaded into the system and/or host controller. Loading thesesthese software routines can be problematic if there is no other source of storage for the routines than the Flash memory of the present invention. For example, if the Flash memory of the present invention contains the BIOS code of the system it is currently installed in. Therefore a secondary source of non-volatile storage that does not require such initialization must exist in systems where in the host controller requires loading of software routines to issue the "STOP" command and allow access of the Flash memory of the present invention.